

**AMENDMENTS TO THE CLAIMS**

Please amend the claims as follows:

1. (Currently amended) A controlled offset amplifier, comprising:
  - (a) an input stage including two transistors, said two transistors having different threshold voltage implants, said two transistors operating at substantially the same current; and
  - (b) an amplification stage that receives a signal from said input stage and provides an output signal related to said signal, wherein the threshold voltage implants of the input stage transistors produce controlled offset voltage at an output of the amplification stage.
2. (Previously Presented) The amplifier of Claim 1, wherein one of said two transistors does not have a threshold voltage implant.
3. (Original) The amplifier of Claim 2, wherein said input stage comprises two source coupled transistors.
4. (Original) The amplifier of Claim 1, wherein said transistors are p-channel MOS transistors.
5. (Original) The amplifier of Claim 2, wherein said transistors are p-channel MOS transistors.
6. (Currently amended) A method for forming a controlled-offset amplifier comprising:
  - (a) forming an amplifier stage;

- (b) forming an input stage comprised of two input transistors, said transistors operating at substantially the same current; and
- (c) applying a threshold voltage implant to only one of said input transistors, wherein the threshold voltage implant produces controlled offset voltage at an output of the amplifier stage.

7. (Original) The method of Claim 6, wherein said transistors are p-channel MOS transistors.